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Wide bandwidth terahertz mixers based on graphene FETs

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Abstract— In this work, we demonstrate wide RF and IF bandwidth resistive fundamental terahertz mixers based on graphene field-effect transistors. In the RF frequency range of 220-487 GHz, the 3-dB IF bandwidth is 32 GHz and 56 GHz for the mixers based on graphene field-effect transistors with the gate length of 1.2 μm and 0.6 μm , respectively. The highest conversion efficiency is estimated to be -28 dB at local oscillator power of 13 dBm. This shows that resistive fundamental terahertz mixers based on graphene field-effect transistors are promising for ultrawide band-width applications.

I. INTRODUCTION

WITH ever-increasing mobile internet traffic, terahertz (THz) wireless communication technology is attracting great interest, since allowing for larger absolute bandwidth [1,2]. Being key components in any communication system, a THz mixer with wide RF and IF bandwidths is highly demanded. However, the circuit design and fabrication of the wide bandwidth THz mixers are challenging tasks. In recent years, it was shown that the graphene field-effect transistors (GFETs), owing to their linear output characteristics and relatively high carrier mobility, have a great potential for development of the GFET based high-frequency mixers [3-5]. But most of the demonstrated GFET mixers reveals rather narrow RF and/or IF bandwidths [6]. In this work, we experimentally demonstrate the GFET-based resistive fundamental THz mixers with wide instantaneous RF and IF bandwidths.

II. RESULTS

The THz mixers based on the GFETs were fabricated on Si substrate using the high-quality CVD graphene [7]. An optical micrograph of the mixer chip is shown in Fig. 1. Two versions of the mixers based on GFETs with different gate length (L_g) of 0.6 and 1.2 μm and the same total gate width of 20 μm (two 10

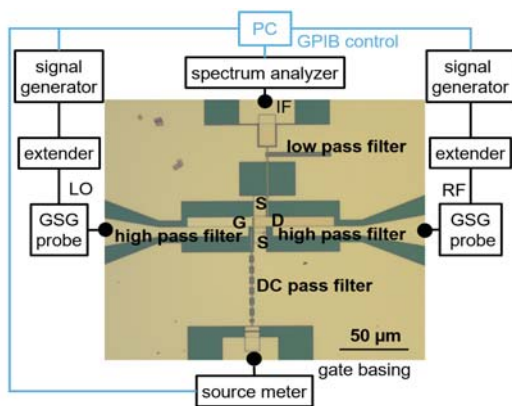


Fig. 1. Optical micrograph of a GFET mixer with L_g of 1.2 μm , together with a schematic block diagram of the on-wafer measurement setup.

μm wide gate fingers in parallel) have been fabricated and analyzed. Coupled line high-pass filters are implemented at the LO and RF ports. The low-pass filter at the IF port consists of a quarter wavelength open stub and stepped-impedance lines. The DC pass-filter is implemented by a high-impedance transmission line. Full-wave EM simulations using CST microwave studio were applied for optimizing layout dimensions. The mixers were characterized on-wafer at room temperature. A schematic block diagram of the on-wafer measurement setup is shown in Fig. 1. The LO and RF signals obtained from Virginia Diodes vector network analyzer extenders were fed to the mixer by T-Wave probes from FormFactor. The IF signals were measured using infinity probes from FormFactor and an FSU50 spectrum analyzer from Rohde & Schwarz. Equipment was controlled remotely by computer via GPIB. The DC performance was measured using a Keithley 2604B Source meter. The S-parameters were measured between the LO and RF ports using a Keysight PNA-X N5247A with a thru-reflect-line (TRL) calibration procedure in the WR 2.2 and WR 3.4 frequency ranges. The TRL calibration kit was designed and fabricated on the same substrate as the GFET mixers.

Fig. 2 shows the measured and modeled drain-source resistance (R_{ds}) versus the gate voltage overdrive from the Dirac point ($V_{gs} - V_{dir}$) of a GFET mixer with L_g of 1.2 μm . The R_{ds} is calculated using the transfer characteristic measured at a drain-source voltage (V_{ds}) of 0.05 V. The electron/hole mobility ($\mu_{e/h}$), the residual carrier concentration (n_0), and the contact resistance (R_c) extracted via fitting the drain resistance model [8], are listed in Table 1.

The corresponding measured and simulated reflection coefficient, $\Gamma = (Z_{in} - Z_c) / (Z_{in} + Z_c)$ ($Z_c = 50 \Omega$), seen looking into the drain port is shown in Fig. 2. The figure shows that Γ data simulated in ADS is in good agreement with the measured data. For a resistive mixer, the maximum conversion efficiency (CE) can be determined from $CE = (\Gamma_{max} - \Gamma_{min})^2 / \pi^2$ [9]. To maximize CE , the difference between Γ_{max} and Γ_{min} should be maximized,

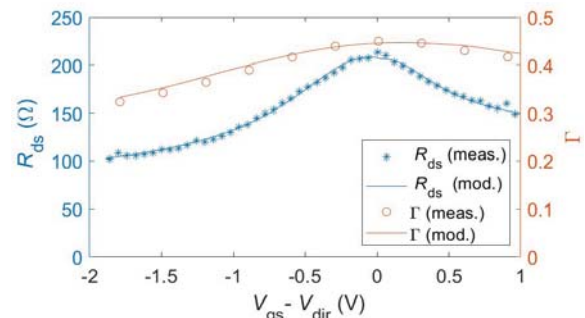


Fig. 2. Measured and modeled R_{ds} and Γ curves versus $V_{gs} - V_{dir}$ at $V_{ds} = 0.05$ V for a GFET mixer with L_g of 1.2 μm .

Table 1 Extracted GFET DC model parameters

	$\mu_{e/h}$ (cm ² /Vs)	n_0 (10 ¹² cm ⁻²)	R_c (Ω)
$L_g=0.6 \mu\text{m}$	3200/3000	1.5	70
$L_g=1.2 \mu\text{m}$	4300/4000	1.3	120

which corresponds to $R_{ds,min} \ll Z_c \ll R_{ds,max}$. Thus, GFETs with high I_{on}/I_{off} ratios are required. In our case, with $\Gamma_{max}=0.45$ and $\Gamma_{min}=0.33$ obtained from the graph, this is far away from the ideal case with $\Gamma_{min}=-1$ and $\Gamma_{max}=1$. One reason is that $R_{ds,min}$ is larger than Z_c due to large contact resistances. In addition, effects of the GFET gate capacitance and parasitic circuit capacitances reduce the difference between Γ_{min} and Γ_{max} at high frequencies.

Fig. 3 shows the measured and simulated conversion efficiency versus the LO power (P_{LO}) for the GFET mixer with L_g of 0.6 μm . High LO power is required to properly pump the mixer between the on and the off states, and to maximize the CE. The measured and simulated CE is up to -50 dB at P_{LO} of -11 dBm. The simulated CE is slightly higher than the measured CE probably due to a shift of the optimal biasing point. The highest CE of the GFET mixers is simulated to be -28 dB at P_{LO} of 13 dBm, which is already approaching to the best semiconductor analogues [10]. To compete with other technologies, further optimizations are required. As it can be seen from Fig. 3, the CE can be as high as -21 dB assuming 2 times lower contact resistance, which is possible to achieve using our recently developed technology [7].

Plots of the normalized conversion efficiency of the GFET mixers with $L_g=0.6 \mu\text{m}$ versus the IF frequency with LO frequency of 220 GHz are shown in **Fig. 4**. The corresponding data for a GFET mixer with $L_g=1.2 \mu\text{m}$ is also shown in **Fig. 4** for two LO frequencies, 220 GHz and 438 GHz. It can be seen that the IF bandwidth is almost the same for the two LO frequencies owing to the wideband design of the mixer. The 3-dB IF bandwidth (f_{3dB}), extracted by fitting the measured CE with $CE=CE_0-20\log(f_{IF}/f_{3dB})^2$, was found to be around 56 GHz and 35 GHz for the GFETs with L_g of 0.6 μm and 1.2 μm , respectively. These f_{3dB} values are comparable with those of the mixers made by semiconductor technologies, such as silicon CMOS mixers and Schottky-barrier diode THz mixers [10, 11].

III. SUMMARY

Using a wideband design, we have demonstrated that it is

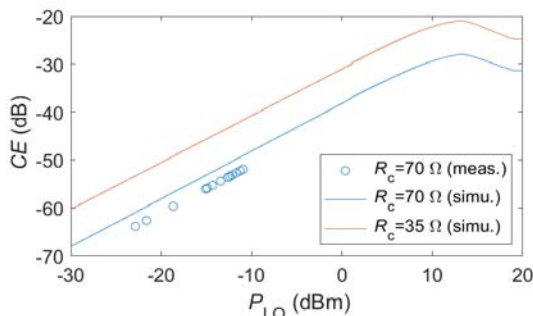


Fig. 3. Measured and simulated conversion efficiency versus LO power of the mixer with L_g of 0.6 μm at $V_{gs}-V_{dir}=-0.5 \text{ V}$, with an LO frequency of 220 GHz and an RF frequency of 221 GHz. Also shown is a simulation of what the conversion efficiency would be with half the contact resistance (top curve).

possible to develop GFET based THz mixers with wide RF and IF bandwidths, simultaneously. Therefore, we believe that this work will serve as platform for further development and corresponding improvements of GFET based mixers in terms of conversion efficiency, linearity and noise performance.

IV. ACKNOWLEDGMENT

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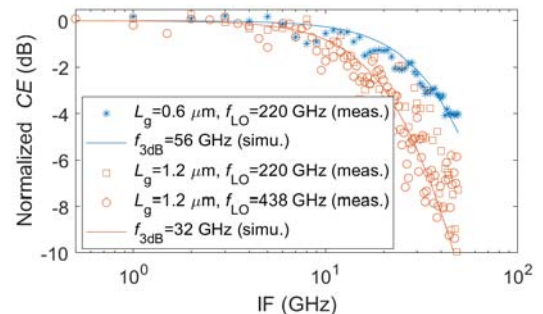


Fig. 4. Normalized conversion efficiency versus IF frequency. Symbols: measured data with zero gate bias and without drain bias. Lines: simulated data. For measurements obtained at an LO frequency of 220 GHz the RF frequency was varied from 221 GHz to 269 GHz, while for measurements obtained at an LO frequency of 438 GHz the RF frequency was varied from 439 GHz to 487 GHz.