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# High Gain V-Band Planar Array Antenna Using Half-Height Pin Gap Waveguide

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**Abstract**—With growing demand for mm-Wave applications, gap waveguide technology introduced many advantageous features compared to hollow waveguides or SIW. Till now several wideband, high-efficiency and highly directive planar gap waveguide antennas have been proposed. Recently, a new form of pins, the so called half-height pin, is proposed for realizing gap waveguide technology. In this paper, a wide-band, high gain, and high efficiency  $8 \times 8$ -element slot array antenna for 60 GHz band based on the new form of pins is introduced. The simulation shows a very good performance of the antenna, with 14% bandwidth of the 10 dB return loss, 26 dBi realized gain and close to 80% aperture efficiency. The antenna has less difficulty in manufacturing because of new pin form and therefore is suitable for the low cost mass production of mm-Wave antennas.

**Index Terms**—gap waveguide, half-height pin, planar slot array.

## I. INTRODUCTION

The rapidly growing demands for high data rate communication systems leads to usage of higher frequency bands. Recently the 60 GHz band is very attractive due to the huge unlicensed bandwidth (up to 7 GHz) available worldwide [1]. One of the challenges for mm-Wave wireless systems is to develop a highly directive planar antenna with high aperture efficiency.

Substrate Integrated Waveguide (SIW) and microstrip arrays are candidates for mm-Wave planar antennas with low profile. However, because of dielectric losses at mm-Wave frequencies they have low aperture efficiencies [2], [3]. Waveguide slot arrays are high efficiency antennas which do not suffer from dielectric losses. However, corporate feed networks in wide-band waveguide slot arrays are so complicated and bulky that at mm-Wave frequencies the manufacturing process is very complicated and expensive [4], [5].

The newly introduced gap waveguide technology is a promising way to design a high-gain and high-efficiency planar slot array antenna for mm-Wave applications [6]–[11]. Low cost manufacturing process of gap waveguide technology compared to hollow wide-band waveguides antennas at mm-Wave frequency bands is a considerable advantage. No need for electrical contact between antenna layers solves many fabrications problems, especially in the mass production of the antenna.

In gap waveguides, PEC-PMC (Perfect Electric Conductor-Perfect Magnetic Conductor) parallel plate waveguide creates a stop band while a textured surface, such as the bed of

the nails or mushrooms, realizes a PMC plate. The textured surface incorporates a guiding structure that can be a ridge, groove or strips. Thus gap waveguides can be categorized into three forms [12]–[15]. The PMC surfaces are usually of a quarter wavelength texture of pins which can achieve a wideband performance. The gap between two plates should be smaller than a quarter of the wavelength to prevent any wave propagation in the gap so that the waves can be propagated only along the guiding structure such as a ridge.

Molding and die pressing or die-sink Electrical Discharge Machining (EDM) are the methods of fabrication gap waveguide Components among other manufacture technologies. The shape of pins in the textured surface of a gap waveguide can be different but the pins length should be always close to a quarter wavelength long. Recently a new form of pins, called as the half-height pins with a height of an eighth wavelength is proposed [16]. Half-height pins have approximately the same stop bandwidth and performance as the full height (quarter wavelength) pins, and because of shorter length, the fabrication of this type of pins is less expensive, especially for mass production goal.

Thus gap-waveguide components such as antenna arrays, couplers and filters [17]–[21], or packaging structures [22] can be redesigned with the new half-height pins with little efforts. In [16] and [23] the idea of half-height pins has been verified for a bend transmission gap-waveguide line.

In this paper, an  $8 \times 8$  planar slot array antenna based on half-height pins gap-waveguide technology for 60 GHz is proposed. For corporate feed network, ridge gape-waveguide with half-height pins is designed. In distribution network layer half-height pins are located on both the upper and the lower plate but the ridge locates only in the lower plate.

First, a  $2 \times 2$  sub-array antenna is designed and simulated in an infinitely large array environment. Then, it is extended to a  $4 \times 4$  sub-array with a 4-way corporate feeding network and complete  $8 \times 8$  slot array antenna is finalized in the last step. The slots shape of the final antenna has been modified optimally for manufacturing. Although, the antenna is still composed of three unconnected layers as the previous antenna with the full-height pins, the new half-height pins reduce the difficulty of fabrication.

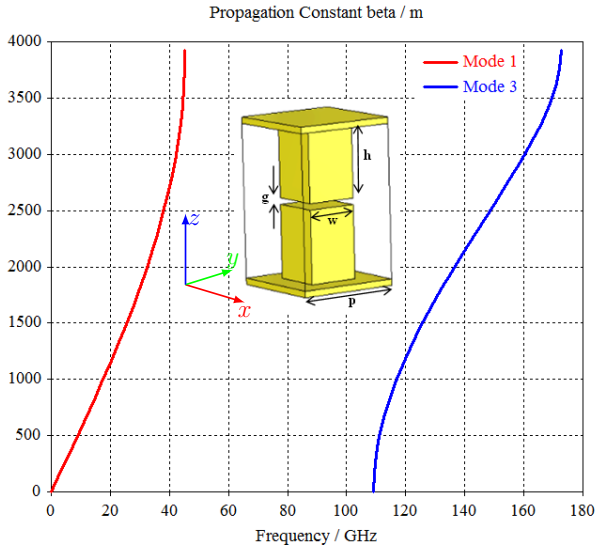


Fig. 1. Unit cell for half height pin and its simulated stop band.

TABLE I  
DIMENSIONS OF  $8 \times 8$ -ELEMENT ARRAY ANTENNA PROTOTYPE FOR MANUFACTURING

Component	Parameter	Value (mm)
Outer Radiating Slot	Length	2.75
	Width	2.3
	Depth	0.5
Inner Radiating Slot	Length	2.6
	Width	1.0
	Depth	0.5
Top Layer	Thickness	1.58
	Length	40
	Width	40
Coupling Slot	Length	2.79
	Width	0.9
	Depth	1.0
Middle Layer	Thickness	2.23
Bottom Layer	Thickness	4.0
Corporate feed Ridge	Width	1.0
	Height	1.3
Feed Waveguide	Length	3.8
	Width	1.9
Pin	Width ( $W$ )	0.4
	Air Gap ( $g$ )	0.05
	Pin Period ( $p$ )	0.8
	Top Part Height	0.66
	Bottom Part Height	0.57

## II. HALF HEIGHT PIN GAP-WAVEGUIDE

Fig. 1 shows a half-height pin unit cell configuration. As it is named, pin length is only one eighth wavelength long, a half of the previous full-height pin. As there is an air gap between the upper and the lower pins, two plates are not electrically connected. The length of the pin is denoted by  $h$ . Pin width and the gap are  $w$  and  $d$  respectively. The pin periodicity is  $p$  in both  $x$ - and  $y$ - direction.

The Eigen-mode solver in CST Microwave Studio has been applied to this basic unit cell of the half-height pin configuration for obtaining the stop-band characteristics. The dispersion diagram of the infinite pin array is shown in Fig. 1. The stop-band is approximately from 45 GHz to 110 GHz.

Antenna feeding network is in the form of ridged gap

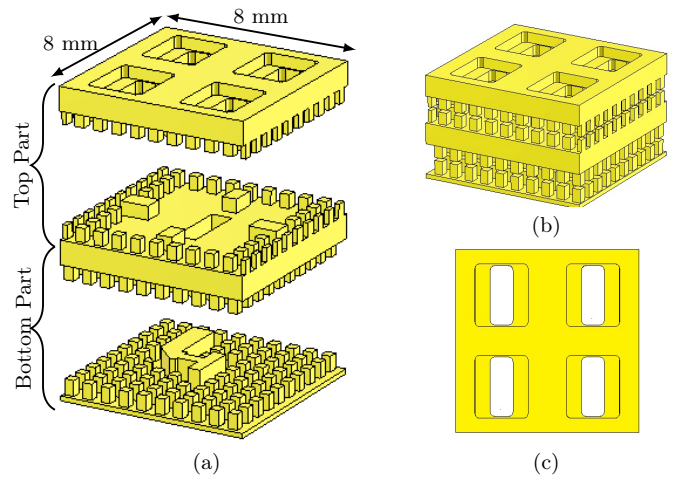


Fig. 2. a) Three layers of  $2 \times 2$  sub-array antenna configurations, b) the unit cell, and c) the top view

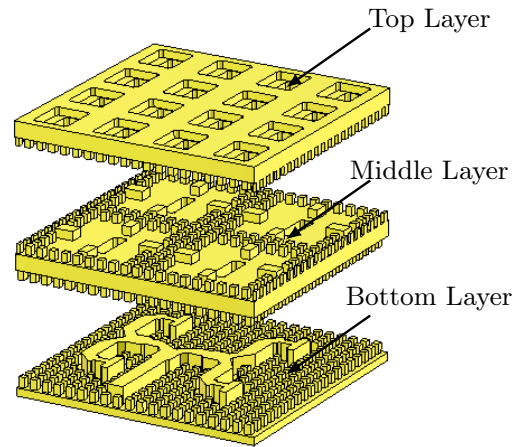


Fig. 3.  $4 \times 4$  slot sub-array configuration with 4-way corporate feeding network.

waveguide. A single ridge is located on the bottom layer and double pins placed on both plates. Pin dimensions are listed in Table I.

## III. CAVITY BACKED SLOTS SUB-ARRAY

Antenna geometry consists of two parts. The bottom part is the part of the corporate feeding network and the top part is the cavity backed slot array.

Initially a  $2 \times 2$  sub-array, as shown in Fig. 2, is designed and optimized in the infinite array environment by using CST Microwave studio. A T-shaped impedance matching sections is added to obtain better coupling between power divider network and slot array, and consequently to improve the reflection coefficient of the antenna.

As we use infinite array approach, mutual coupling between sub-arrays included in the results. Therefore, the simulation can be a good approximation of the final antenna performance. The slots spacing and dimensions are optimized to achieve high possible gain with consideration of grating lobes prevention.

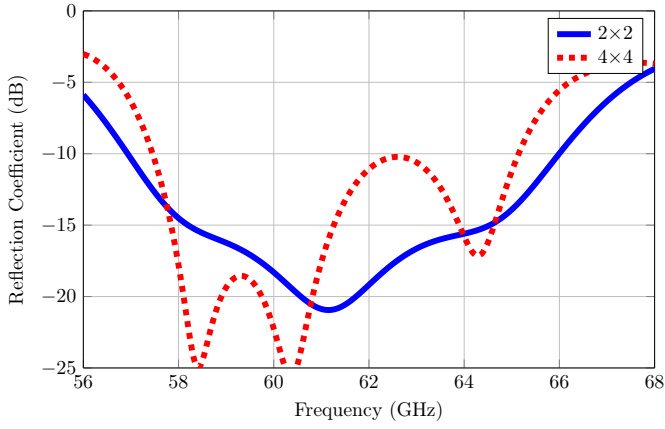


Fig. 4. Simulated reflection coefficient for the  $2 \times 2$  and the  $4 \times 4$  slot sub-array.

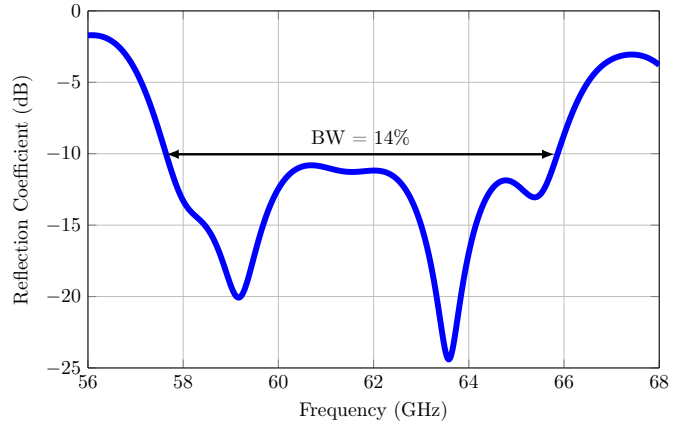


Fig. 6. Simulated reflection coefficient of final  $8 \times 8$  antenna.

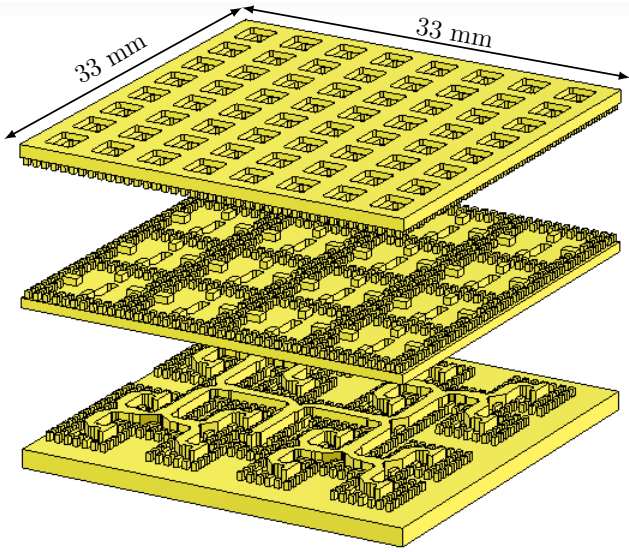


Fig. 5. Geometry of planar  $8 \times 8$ -element slot array antenna.

The height of the radiating slots has been increased in this design compared to the full-height pin design due to manufacturing restrictions on the thickness of the top layers. A thicker depth of the slots degrades reflection coefficient performance while a thinner layer is hard to manufacture.

After designing a  $2 \times 2$  sub-array, a 4-way corporate power divider is designed and the antenna extended to a  $4 \times 4$  slot array antenna. Fig. 3 shows the  $4 \times 4$  antenna sub-array. Full wave simulation has been applied and the results of both the  $2 \times 2$  array and the  $4 \times 4$  array are illustrated in Fig. 4.

#### IV. $8 \times 8$ PLANAR SLOT ARRAY ANTENNA CONFIGURATION

Fig. 5 shows the final antenna configuration, which consists of three unconnected layers. In the bottom part, power divider is implemented in the form of the half height pin ridge gap-waveguide. the mid-layer contains half top pins of feeding network and half bottom pins of cavity backed slots layer. The top layer also has slots and top half pins of the cavity part.

The total size of the antenna is  $33 \times 33 \times 7.8 \text{ mm}^3$ . The final antenna has a fewer number of pins in distribution feeding network part which is beneficial for manufacturing and mass production of the antenna.

The final antenna reflection coefficient is shown in Fig. 6 and the antenna exhibits an impedance bandwidth ( $\text{VSWR} \leq 2$ ) of 14% from 57.5 to 66 GHz.

The full wave simulated radiation patterns of the antenna at 58, 62 and 66 GHz in the  $E$ - and the  $H$ -planes are plotted in Fig. 7. The radiation patterns are symmetrical and the first sidelobe levels in both  $E$ - and  $H$ -planes are around -13 dB.

Fig. 8 depicts the directivity and the realized gain of the antenna versus frequency. The dashed straight lines show the maximum available directivity of an aperture with the dimension of  $33 \times 33 \text{ mm}^2$  when the aperture efficiency is 60% to 90%.

The antennas parameters are listed in Table I. Pins in both parts have the same size except for their heights.

#### V. CONCLUSION

A planar slot array antenna with corporate power divider based on half-height pin gap-waveguide technology for V-band applications is introduced.

As long as any electrical connection between antenna layers is not required it has less complicated fabrication process. Half-height pin adds additional benefits of having shorter pins for easier manufacturing. The half-height pin antenna shows good performance. Simulated realized gain is close to 26 dBi over the entire operation bandwidth from 58 to 66 GHz and the total radiation efficiency is higher than 75%. New fabrication methods such as die-sink EDM or die forming can provide us low cost mm-Wave antenna for 5G application.

The new form of pins is promising for gap waveguide slot array antennas though for larger antennas the air gaps in the middle of pins may result in slight more attenuation.

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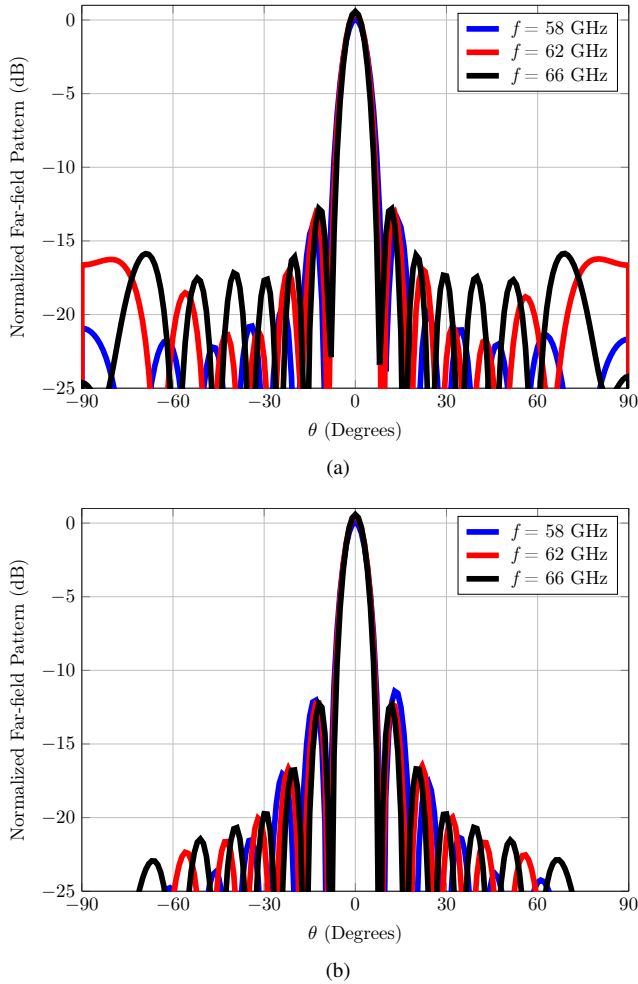


Fig. 7. Radiation pattern of 8x8-element slot array antenna in the a)  $E$ -plane, and b)  $H$ -plane.

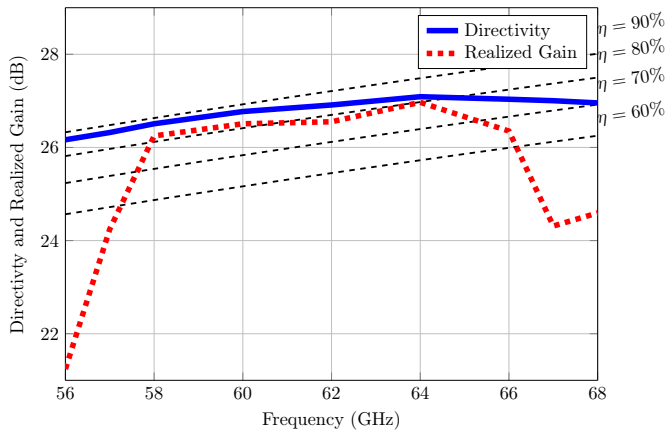


Fig. 8. Directivity and realized gain of simulated antenna.

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