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A Corrugated Planar-Goubau-Line Termination for Terahertz Waves

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Abstract—The planar Goubau line is a promising low-loss metal waveguide for terahertz applications. To enable advanced circuits and multi-port measurements based on planar Goubau lines, there is a strong need for broadband impedance-matched loads, which can be used to absorb the energy and minimize standing waves in a system. In this work, we propose a termination for planar Goubau lines based on an exponentially-tapered corrugated line, gradually increasing conductor losses while maintaining small reflections. The corrugation density is high enough to increase conductor losses without requiring an auxiliary low-conductivity material. A 400-µm long planar Goubau line load was fabricated on a 10- μ m thick silicon substrate suspended in the air. Simulations of the load show excellent agreement with calibrated reflection measurements in the frequency range 0.5 THz - 1.1 THz. Above the cut-off frequency of around 580 GHz, the measured reflections are less than -19 dB, below the noise floor of the characterization setup.

Index Terms—Corrugated line, Matched load, On-wafer measurements, Planar Goubau line, Scattering parameters, Silicon-on-insulator, Suspended substrate, Submillimeter wave circuits, Terahertz waveguides

I. INTRODUCTION

E FFICIENT and broadband planar waveguides are essential for integrated photonic and electronic systems [1]. However, traditional planar multi-conductor lines have a relatively high loss at terahertz frequencies. One alternative is to use a single-conductor planar waveguide, the planar Goubau line (PGL). The PGL consists of a single conducting strip deposited on a dielectric substrate [2]. Similar to other single-wire waveguides like the Sommerfeld's wire [3] and the Goubau line [4], the PGL propagates a quasi-TM (hybrid EH) surface wave with a field that decays away from the surface of the metal strip. The PGL has a low attenuation constant compared to other metal waveguides when designed on an electrically-thin substrate [5], making it an efficient alternative for guiding terahertz waves. Although several components have been proposed for PGL, the performance needs to be improved for using single-wire interconnects in applications.

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Fig. 1: Illustration of the tapered corrugated planar-Goubau-line load. The figure shows the orientation of the electric (black) and magnetic (blue) fields, and current densities (red). The fabricated load has $s = 10 \,\mu\text{m}$, $w_c = 1 \,\mu\text{m}$, $w_g = 1 \,\mu\text{m}$ and l_c going from $1 \,\mu\text{m}$ to $50 \,\mu\text{m}$ exponentially.

In recent years, some circuit elements-such as antennas [6], resonators [7], and filters [8]-have been proposed for PGL, including calibration standards [9]. A vital circuit element is an impedance-matched load, which allows terminating ports with low reflections, enabling components and multi-port scattering-parameters measurements. Matched loads can be done using lumped resistive elements at microwave frequencies. Still, their fabrication becomes increasingly difficult at higher frequencies, requiring other methods at THz frequencies, such as distributed elements along the waveguide. Some matched-load designs have been proposed for PGL at frequencies between 50 GHz and 500 GHz. Xu et al. [10] suggested a PGL load was made by terminating a line with a PGL-CPW transition with a short-circuited titanium-film resistor. Treizebre et al. [11] proposed using a tapered nickel-chromium high-resistance sheet below the PGL, and Le Zang et al. [12] proposed using a low-conductivity nickel PGL with hook-shaped corrugations on one side of the line.

All these proposed loads partly rely on the low conductivity of a second thin-film material to increase the ohmic losses, which adds complexity to the fabrication process. In this paper, we propose and demonstrate an impedance-matched load based on a corrugated PGL [13], which has been smoothly-tapered to minimize reflections and has a high corrugation density to increase ohmic losses, see Fig. 1.

II. METHOD

Corrugated waveguides can support TM surface waves if the corrugation length, l_c , is less than a quarter-wavelength, $\lambda_c/4$,



Fig. 2: Simulated magnetic-field intensity of the PGL load. The field is shown at 500 GHz (below the cut-off frequency), 600 GHz (above the cut-off) and 1000 GHz (well above the cut-off). The attenuation of waves increases dramatically when the corrugation length is close to $l_c = \lambda_0/(4n_c)$, where n_c is the effective refractive index of the corrugations.

of the wave in the corrugation [14]. When the corrugations are almost a quarter-wavelength long, the phase constant, the field confinement, and the conductor losses dramatically increase [14]–[16]. The conductor losses result from the surface currents induced by the difference of the magnetic field in the conductor's boundary, as stated by Ampère's law. The power loss in the conductor can be calculated as [17]:

$$P_c = \int_S \frac{1}{2} \operatorname{Re}\{Z_m\} |\vec{H}_t|^2 dS \tag{1}$$

where H_t is the magnetic field tangent to the conductor, $Z_m = \sqrt{j\omega\mu/\sigma}$ is the intrinsic impedance of the metal, j is the imaginary unit, ω is the angular frequency, and μ and σ are the metal's permeability and conductivity, respectively. Conductor losses can be maximized at a given frequency by increasing the metal's μ/σ ratio or by increasing the surface of the metal exposed to the tangential magnetic field. Thus, by using higher corrugation length, l_c , and lower corrugation width, w_c , and gap, w_g , the ohmic losses are increased dramatically without needing a second material with a lower μ/σ ratio, simplifying the fabrication process. A sudden increase of losses in an electrically-short distance results in an input impedance close to a short circuit, increasing reflections. Therefore, l_c needs to increase gradually to minimize reflected power losses. Moreover, a gradual increase in l_c will increase the absorbing bandwidth of the corrugated PGL. Reflections are also lowered if the magnetic-plane symmetry of the PGL's propagating mode is respected by corrugating both sides of the PGL. Finally, the proposed load's minimum and maximum frequency of operation can be tuned by choosing the longest and shortest l_c , respectively, where the w_c and w_g must be as narrow as possible to increase losses. In this work, the



Fig. 3: Micrograph of the fabricated PGL load. A CPW-PGL mode converter was used to excite the PGL using on-wafer ground-signal-ground probes. The Green dashed line shows the measurement reference plane. The inset shows a zoom-in micrograph of the load.

load design exhibits a corrugation length, l_c , that increases exponentially from 1 µm to 50 µm long, over a length of 400 µm, whereas the corrugation width, w_c , and gap, w_g , are both 1 µm, two orders of magnitude smaller than the wavelength. This results in a lower cut-off frequency of approximately 580 GHz when $l_c = \lambda_0/(4n_c) = 50$ µm, where $n_c = 2.4$ is the effective refractive index of the waves propagating in the corrugations, approximated as a coplanar stripline odd mode. To avoid substrate modes and radiation loss [18], the design was implemented on a 10-µm thick silicon substrate suspended in the air.

Full electromagnetic simulations were carried out using a time-domain solver (CST Microwave Studio). The materials were modeled with full losses: gold with $\sigma = 4.1 \times 10^7$ S/m and high-resistivity silicon with a permittivity of $\epsilon_r = 11.7$ and $\tan \delta = 1.7 \times 10^{-5}$ according to [19]. Fig. 2 shows the magnetic-field intensity for three frequencies: below, above, and well above the cut-off frequency.

The PGL load was fabricated using a silicon-on-insulator SOI) wafer, which consists of a $10-\mu$ m-thick high-resistivity (> $10 k\Omega cm$) device layer, $1-\mu$ m buried oxide layer grown on a high resistivity 400- μ m thick silicon handle layer. First, the front-side PGL circuit pattern was defined using electron-beam lithography followed by evaporation of a 20/350-nm-thick Ti/Au layer. Next, the high-resistivity (> $10 k\Omega cm$) silicon membrane was suspended by etching the bulk silicon below the influence area of the device using a silicon-on-insulator wafer. More details of the fabrication process can be found in [8]. By fabricating the PGLs on an electrically-thin substrate suspended in air, radiation losses drastically drop [5] since the phase velocity of the PGL mode is less than that of the substrate modes [20].

The PGL matched load was characterized by measuring the reflection coefficient between 0.5 THz and 1.1 THz using a vector network analyser (Keysight N5242A) with frequency extenders (VDI WR1.5VNAX and WR1.0VNAX) and DMPI T-Wave ground-signal-ground probes [21], [22] (see Fig. 4). The intermediate-frequency bandwidth was set to 100 Hz.





Fig. 4: Measurement set-up. Close-up picture of the terahertz on-wafer probes and the fabricated chip with the impedance-matched loads. Only one probe is used for the actual reflection measurements, while a two-port TRL calibration was applied.

To excite the PGL mode using ground-signal-ground probes, we included a coplanar waveguide (CPW) mode converter, which maximizes the delivered power by minimizing losses by avoiding small strip and gap dimensions and minimizing reflections by gradually changing the characteristic impedance. The measurements were calibrated with dedicated multi-line Thru-Reflect-Line standards for PGL [9] fabricated on the same chip. Calibrating allows setting the reference plane in the PGL (see Fig. 3) and de-embed the coplanar waveguide transition. The lines used for calibration have an electrical length of $\lambda_g/4$, $3\lambda_g/4$, and $11\lambda_g/4$ at the center frequency of both the WR1.5 and WR1.0 frequency bands. Using PGL calibration standards, we characterized the matched load directly, excluding the influence of the mode converter. The chip was placed on a ceramic chuck during measurements. After S-parameter calibration, the reflection measurement of a PGL calibration line standard indicates a noise floor of around -20 dB as shown in Fig. 5. The suspended membrane circuit is strong enough to support the pressure from on-wafer probing.

III. RESULTS

Fig. 5 shows a comparison of the simulated and measured reflection coefficients against frequency. There is good agreement between simulations and measurements, with a measured reflection coefficient smaller than -19 dB between 0.58 THz and 1.1 THz, and around -25 dB on average. However, the noise floor for reflection measurements in our setup limits the characterization of the load below -20 dB. The presented simulated results of the PGL load made out of a perfect electric conductor (PEC) show a high reflection coefficient, implying that the conductor loss and not radiation loss produce a good impedance match. Both simulations and measurements show a cut-off in the magnitude of the reflections around 0.58 THz. At the cut-off frequency, the length of the matched load is approximately $1.2\lambda_g$, where λ_g is the guided wavelength (PGL), and the longest corrugation length is $0.22\lambda_c$. The electric energy stored at the tips of



Fig. 5: Simulated and measured reflection coefficient (S_{11}) of the PGL matched load against frequency. The measurement of a line calibration standard is included to show the noise floor after TRL calibration

the corrugations can be modeled as being terminated in a capacitive susceptance, and thus the cut-off frequency may be down-shifted [23]. Simulation results of the load show a frequency bandwidth of over a decade, ultimately affected by the onset of substrate modes.

IV. CONCLUSION

In conclusion, we have demonstrated a broadband, impedance-matched load for planar Goubau lines at terahertz frequencies based on a densely and exponentially-tapered corrugated line. In the frequency range between 0.5 THz and 1.1 THz, the measured return loss is at least 19 dB. This load has the advantage of having a simple design and requires no additional low-conductivity thin-film materials. The measurements of the proposed load show high performance compared to the state-of-the-art results, shown in Table I. Future work could apply a detailed parameter analysis and design methodology for the trade-off between return loss, bandwidth, and physical size of the impedance-matched load.

TABLE I: State-of-the-art in PGL loads

References	[10]	[11]	[12]	This work
Frequency (GHz)	40-65	60-325	195-500	580-1100
Return loss (dB)	>12	>13	>20*	>19
Size	$0.16\lambda_{0}^{2}$	$0.03\lambda_0^2$	$0.05\lambda_0^2$	$0.16\lambda_0^2$
Resistive material	Ti	Ni:Cr	Ni	_ 0
*simulation results				

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